

FIG. 1
 (PRIOR ART)

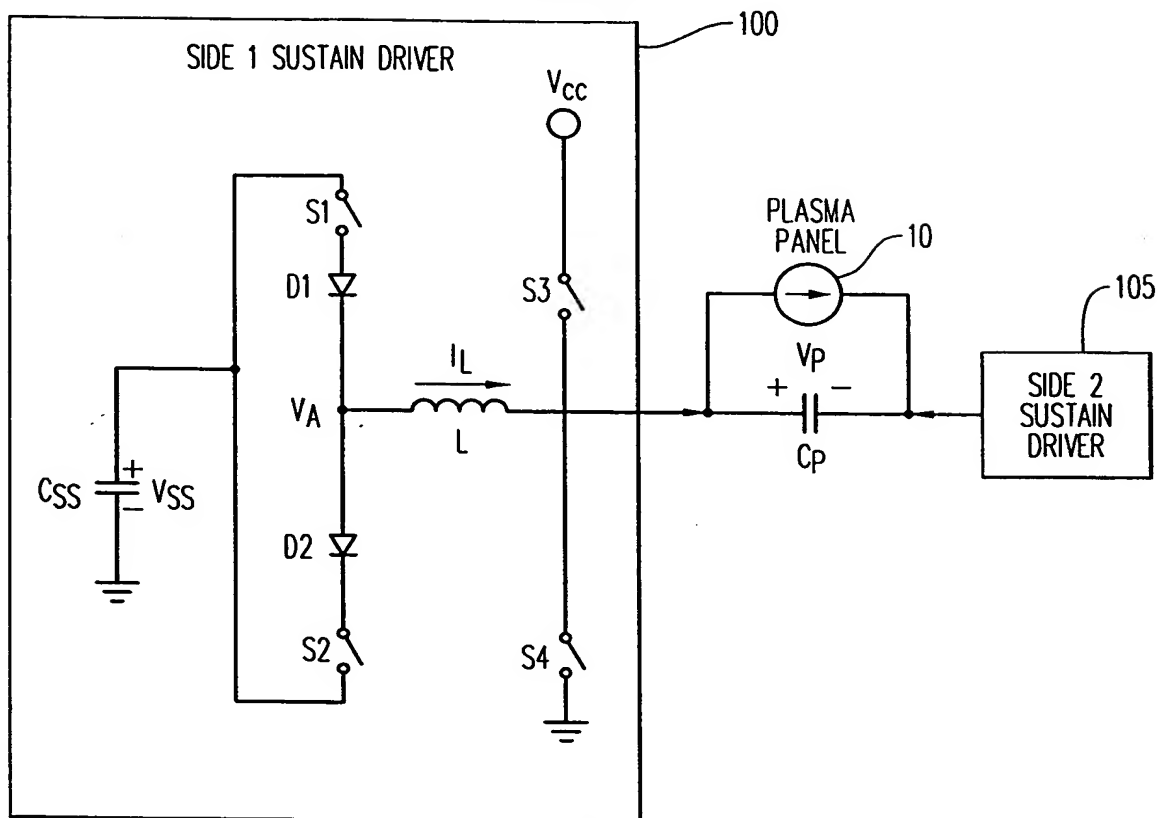
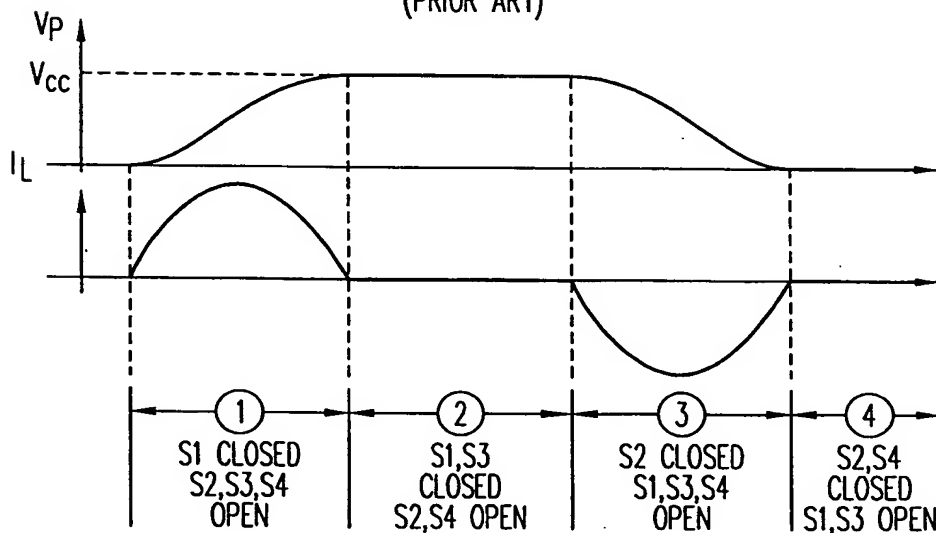


FIG. 2
 (PRIOR ART)



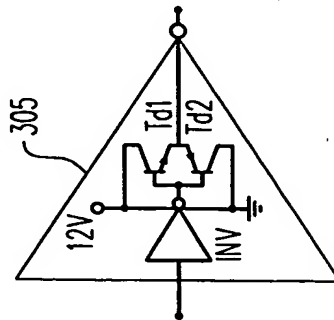


FIG. 3
(PRIOR ART)

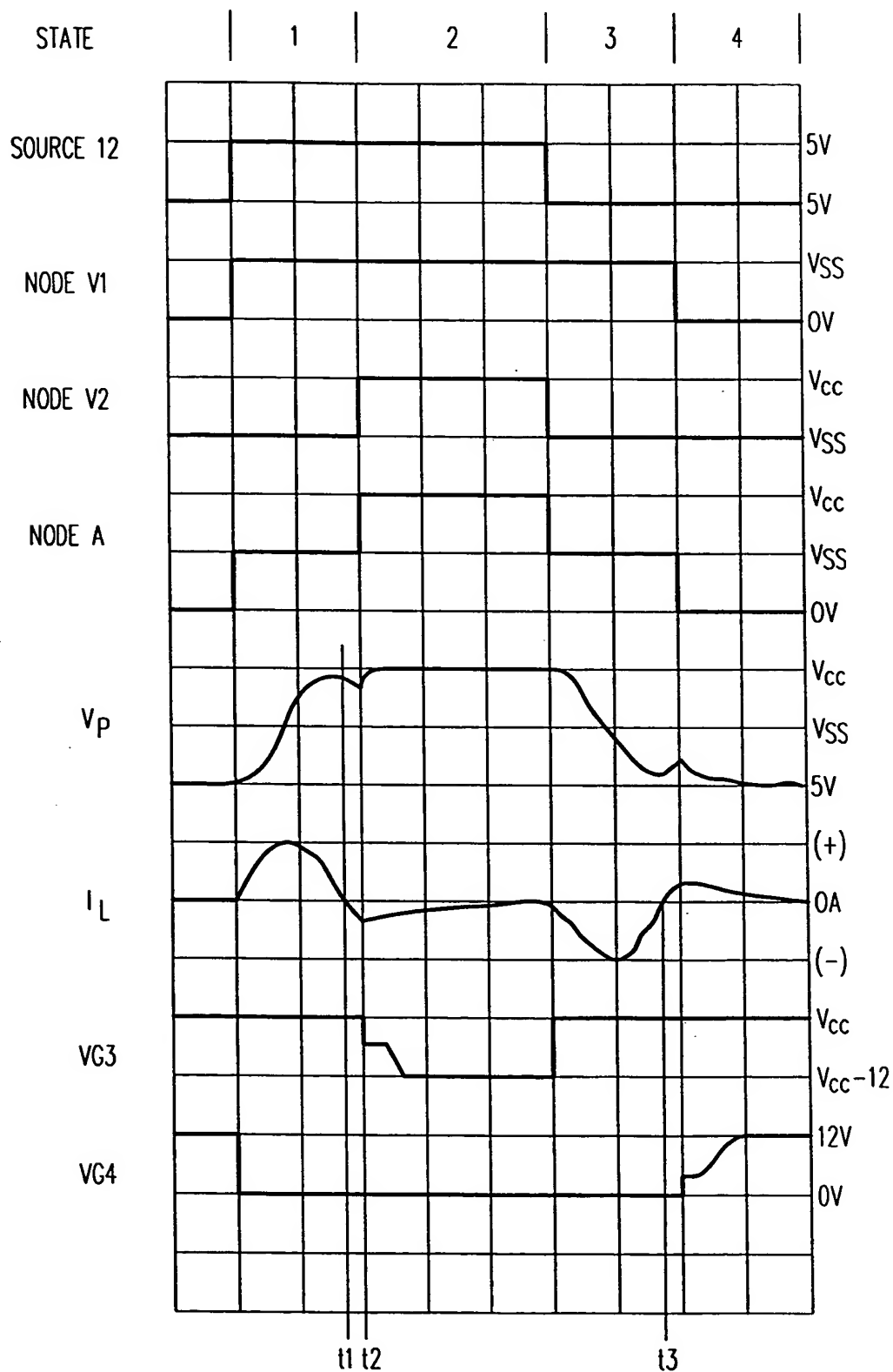
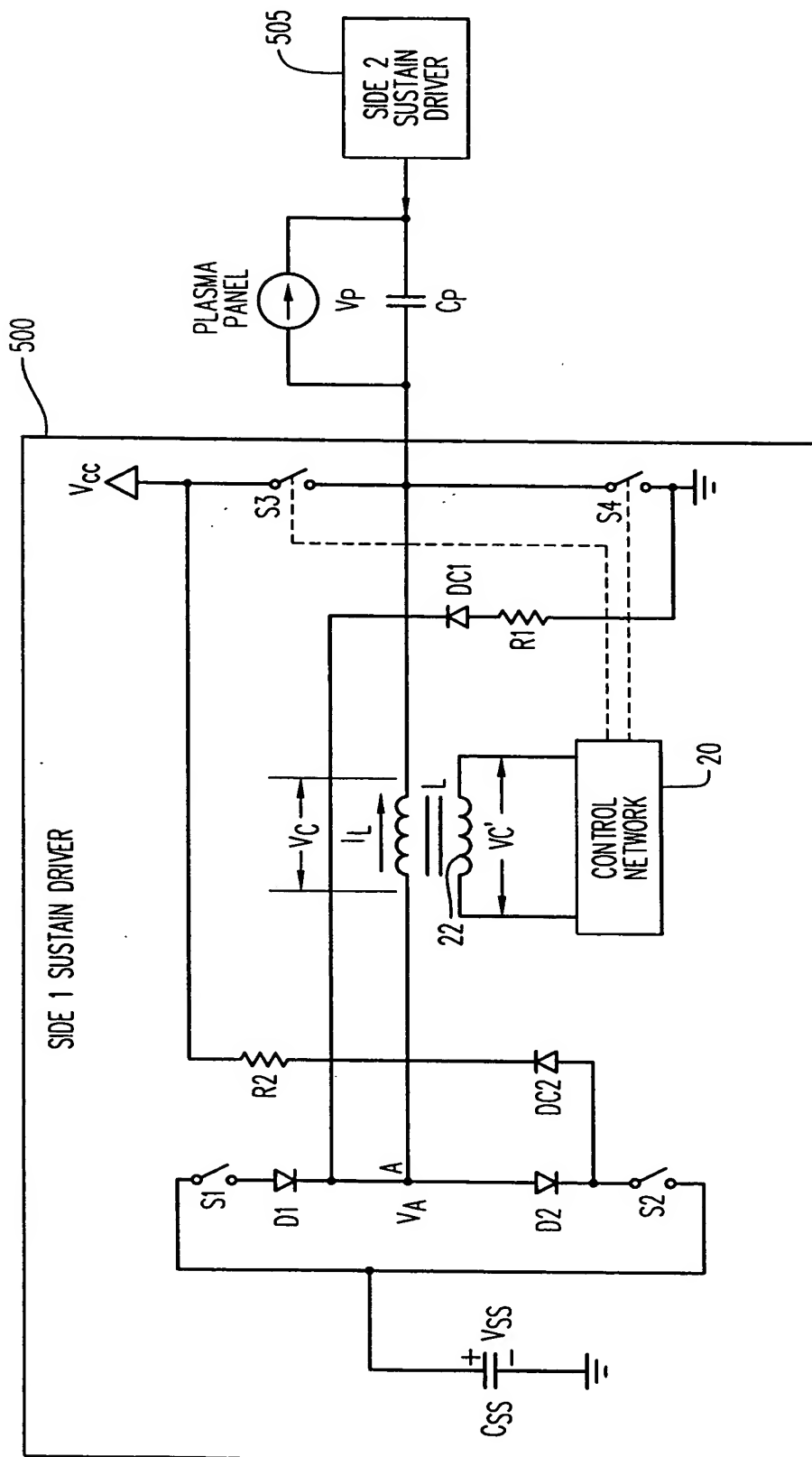


FIG. 4
(PRIOR ART)

FIG. 5
 (PRIOR ART)



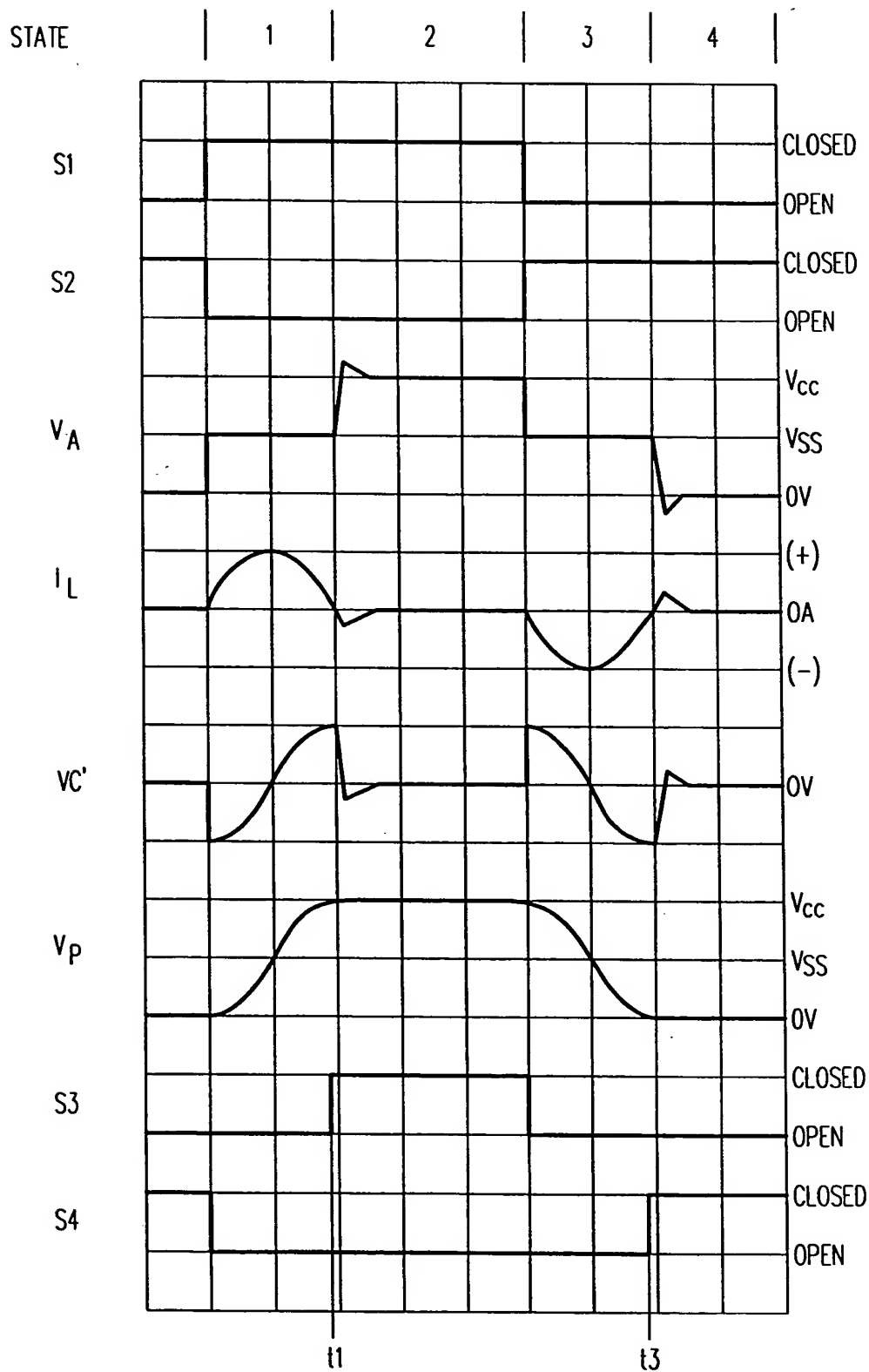


FIG. 6
 (PRIOR ART)

FIG. 7

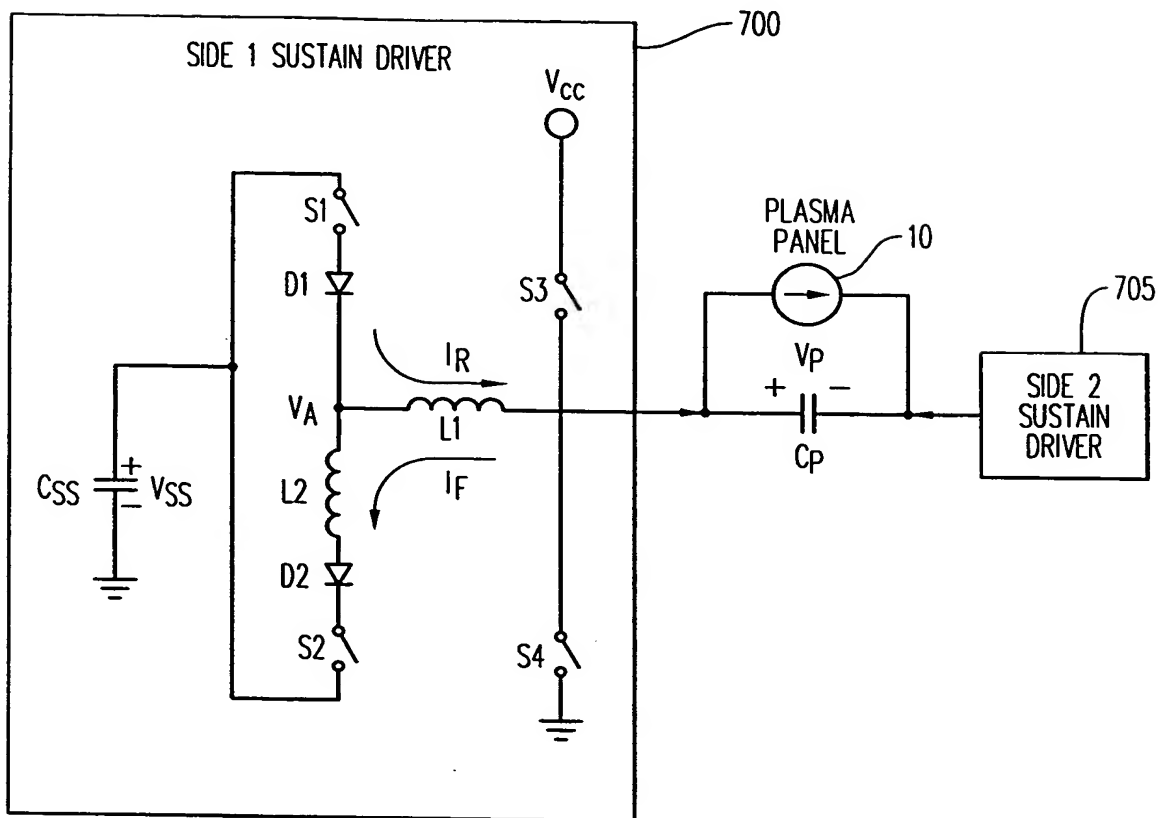


FIG. 8

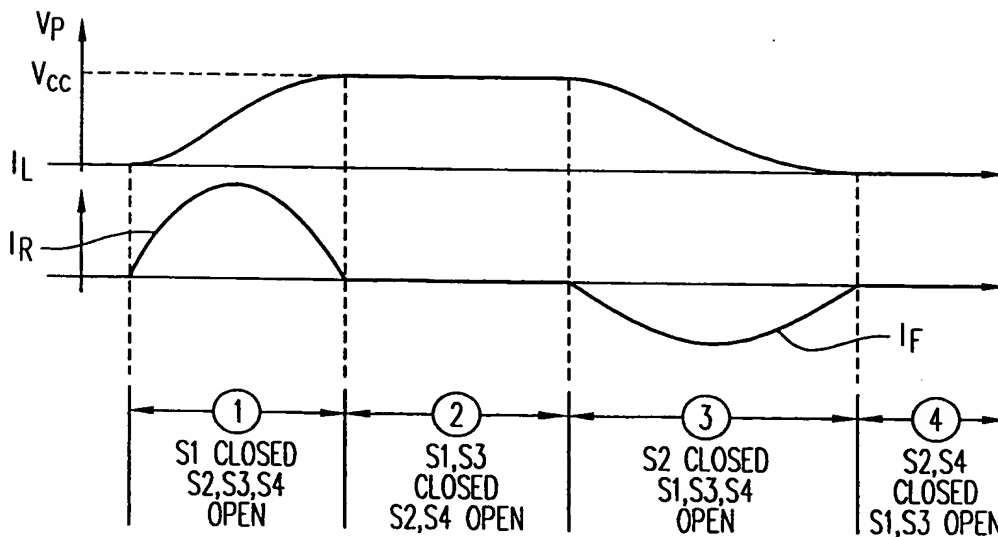
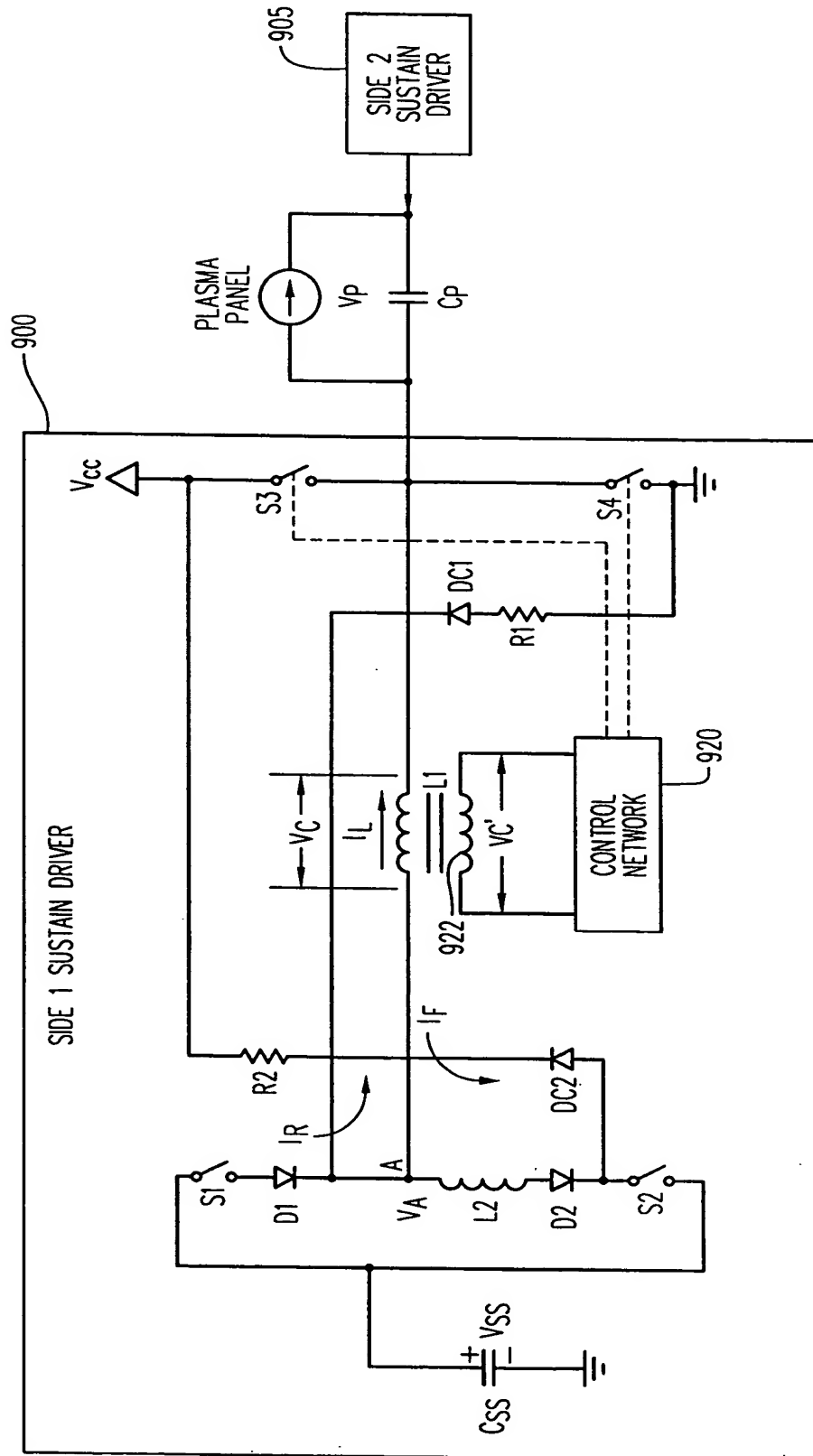


FIG. 9



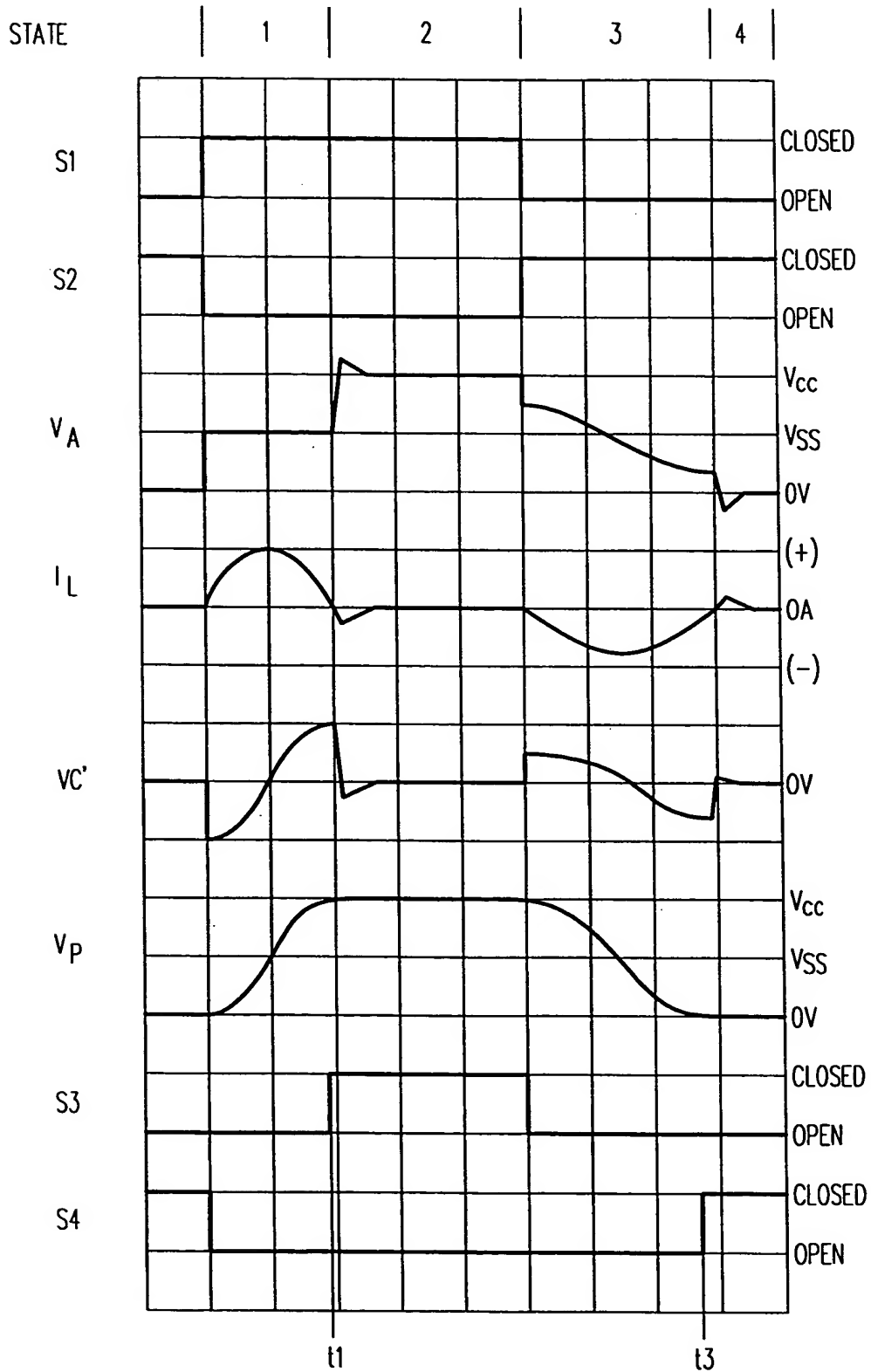


FIG. 10

The diagram illustrates a dual-channel plasma display driver (1100) designed to drive a plasma panel. The system consists of two sustain drivers, labeled 1120 and 1130, which are connected to a common bus line. Driver 1120 is a half-bridge circuit with a high-side MOSFET (922) and a low-side MOSFET (921). The gate of MOSFET 922 is driven by a control network (1120) and a bootstrap capacitor (1132) connected to a V_{CC} supply. The gate of MOSFET 921 is driven by a control network (1130) and a bootstrap capacitor (1131) connected to a V_{SS} supply. The drain of MOSFET 922 is connected to the common bus, and the source of MOSFET 921 is connected to ground. Driver 1130 is a similar half-bridge circuit with a high-side MOSFET (923) and a low-side MOSFET (924). The gate of MOSFET 923 is driven by a control network (1120) and a bootstrap capacitor (1132) connected to a V_{CC} supply. The gate of MOSFET 924 is driven by a control network (1130) and a bootstrap capacitor (1131) connected to a V_{SS} supply. The drain of MOSFET 923 is connected to the common bus, and the source of MOSFET 924 is connected to ground. The common bus is connected to the plasma panel (represented by a circle with an arrow) and a capacitor (C_p). The plasma panel is also connected to a sustain driver (1100) and a V_{CC} supply. The circuit includes various components such as resistors (R₁, R₂), capacitors (C₁, C₂), inductors (L₁, L₂), and diodes (D₁, D₂). The diagram also shows a V_{SS} supply and a V_{CC} supply. The plasma panel is connected to a sustain driver (1100) and a V_{CC} supply. The circuit includes various components such as resistors (R₁, R₂), capacitors (C₁, C₂), inductors (L₁, L₂), and diodes (D₁, D₂). The diagram also shows a V_{SS} supply and a V_{CC} supply.

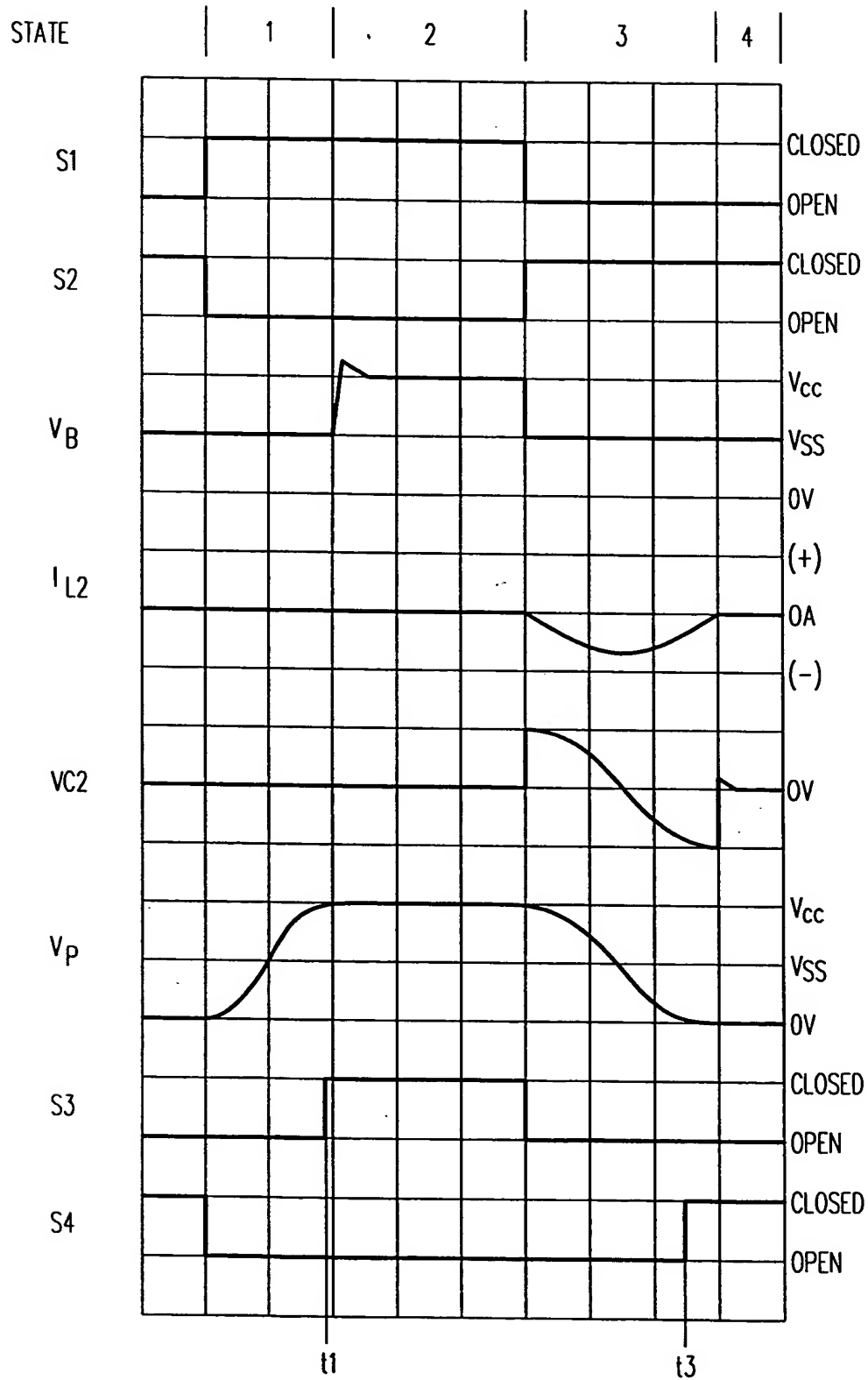


FIG. 12

FIG. 13

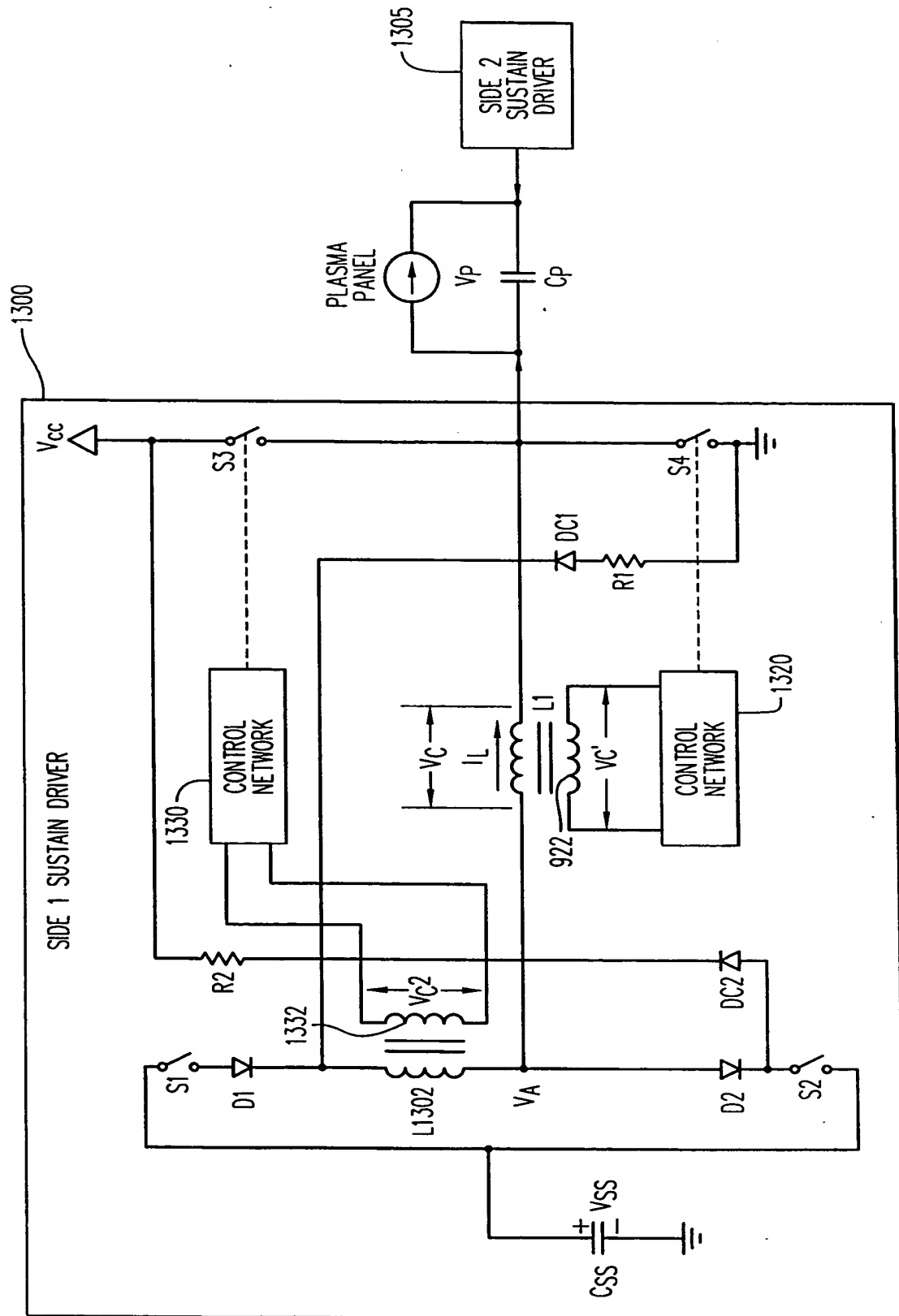


FIG. 14

